Broadband Silicon Absorber of Terahertz Radiation

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Abstract—A three-layer structure is fabricated on a low-resistance silicon substrate using plasma chemical etching. We study it experimentally and show that a substrate with such a structure can be used as an absorber of terahertz (THz) radiation in the frequency range of 0.5–2.0 THz. For this structure, absorbance in the indicated frequency range is measured to be more than 95%.

Keywords: three-layer sub-wavelength antireflective structure, terahertz range, Bosch process DOI: 10.1134/S1027451019060466

INTRODUCTION

Recently, the design and development of efficient absorbers of terahertz radiation has been attracting more and more attention due to their prospective use in terahertz radiation sensors [1]. Terahertz absorbers known to date, which are based on terahertz metamaterials, have relatively narrow absorption bands. In this work, we fabricate an absorber operating in a broad range of THz frequencies and investigate its properties. The absorber consists of a three-level sub-wavelength structure assembled on a low-resistance silicon substrate using plasma chemical etching. Low-resistance boron-doped silicon was chosen as the substrate material because of its high absorption in the THz range. The absorption of THz radiation by the lowresistance silicon substrate increases with increasing substrate thickness, and the transmission of THz radiation decreases to nearly zero as the substrate thickness reaches 200-400 µm. However, the low-resistance silicon substrate as such is not a good THz absorber because of considerable Fresnel reflection at the silicon-air interface. Fresnel reflection losses do not depend on the substrate thickness and amount to 28-30% in the frequency range of 0.5-2.0 THz. To reduce the Fresnel losses and increase absorption, we must use antireflective coatings [2] or antireflective structures [3]. Modeling and calculation of antireflective structures were addressed in works [4, 5].

FABRICATING THE ANTIREFLECTIVE **STRUCTURE**

To ensure absorption in a broad range of frequencies, we created a three-layer sub-wavelength antireflective structure on one side of the silicon substrate using plasma chemical etching (the Bosch process [6]).

The fabricated three-layer antireflective structure had the following parameters: the structure period was $100 \,\mu\text{m}$; the (square) side of the lower layer was $80 \,\mu\text{m}$; the side of the upper layer was $45 \,\mu\text{m}$; the height of the lower layer was 40 µm; and the height of the upper layer was 40 µm (Fig. 1). The results of numerical modeling reported in [5] showed that an antireflective structure with such parameters reduces Fresnel reflection to below 5% for radiation in the range of 0.50-2.58 THz.

The substrate was a disk of *p*-silicon with the resistance $\rho = 0.54 \Omega$ cm; it had a diameter of 50 mm and a thickness of 500 µm. The process of fabrication of the antireflective structure included the following stages:

(1) coating the entire substrate surface with an aluminum layer with a thickness of 50 nm using physical vapor deposition;

(2) creating an aluminum mask for the upper layer, corresponding to a square side of 45 µm, on the substrate surface by photolithography (an FP-2512 photoresist was used);

(3) removal of the FP-2512 photoresist residues and application of a layer of FP-3520 photoresist on top of the created aluminum mask;

(4) applying a pattern with a square side of 80 um and creating a photoresist mask for the lower level by photolithography;

(5) applying the Bosch process to etch the silicon to a depth of 40 µm through the photoresist mask of the lower level;



(b)



Fig. 1. Electron microscopy images of the fabricated antireflective structure.

(6) removal of residues of the lower-level photoresist mask by etching in oxygen plasma and 1-methyl-2-pyrrolidone (NMP);

(7) applying a Bosch process to etch the silicon to a depth of 40 μ m through the upper level aluminum mask;

(8) removal of the aluminum mask residues.

Plasma chemical etching (Bosch process) was carried out using an ETNA-100-PT unit (NT MDT, Zelenograd, Russia). An electron microscopy image of the fabricated antireflective structure is shown in Fig. 1.

EXPERIMENTAL CHARACTERIZATION OF THE FABRICATED ABSORBER

The fabricated absorber was studied by recording its reflection and transmission spectra using a Tera K8 system (Menlo Systems GmbH, Germany) for terahertz time-domain spectroscopy. The working range



Fig. 2. Experimental setup.



of the spectrometer spans frequencies from 0.2 to 2.5 THz. The spectral resolution was 5 GHz. The experimental setup was assembled so that both transmission and reflection measurements can be performed without the need to dismount the unit and remove the sample between measurements (Fig. 2). Transmission and reflection measurements were carried out at the normal incidence angle. Absorbance was calculated by the formula A = 1-T-R, where *T* is the transmittance and *R* is the reflectance. The measured absorbance was 95% in the range of 0.5–2.0 THz. We also found that the fabricated three-layer structure functioned as a broadband antireflective coating in the THz range (Fig. 3). Fresnel reflection in the working-frequency range fell from 28–30% to below 3%.

CONCLUSIONS

We studied a three-layer structure fabricated on the surface of low-resistance silicon using plasma chemical etching. The measured absorption was found to be more than 95% in the frequency range of 0.5–2.0 THz. The fabricated element can therefore be used in terahertz radiation sensors as a broadband spectral absorber operating in the terahertz range. Additionally, similar structures can be used to reduce Fresnel losses from the surfaces of transmissive silicon-based optical elements operating in the terahertz range [2, 7] (in this case, high-resistance silicon must be used as the substrate).

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Fig. 3. (a) Absorption and (b) reflection spectra of the fabricated sample.

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